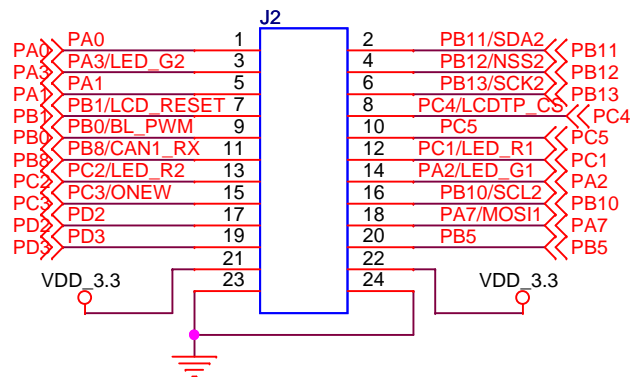
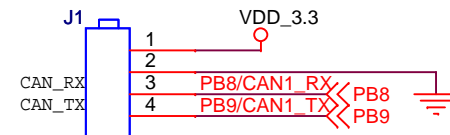


ETHERNTER MODULE ONLY 107

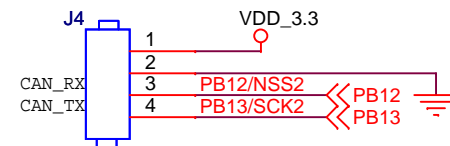


- | | |
|----------------|-----------------|
| 1. MII_CRS | 2. COM_TX_EN |
| 3. MII_COL | 4. COM_TXD0 |
| 5. MII_RX_CLK | 6. COM_TXD1 |
| 7. MII_RXD3 | 8. COM_RXD0 |
| 9. MII_RXD2 | 10. COM_RXD1 |
| 11. MII_TXD3 | 12. COM_MDC |
| 13. MII_TXD2 | 14. COM_MDIO |
| 15. MII_TX_CLK | 16. RX_ER |
| 17. GPIO1 | 18. COM_RX_DV |
| 19. GPIO2 | 20. COM_PPS_OUT |
| 21. VDD_3.3 | 22. VDD_3.3 |
| 23. GND | 24. GND |

CAN MODULE



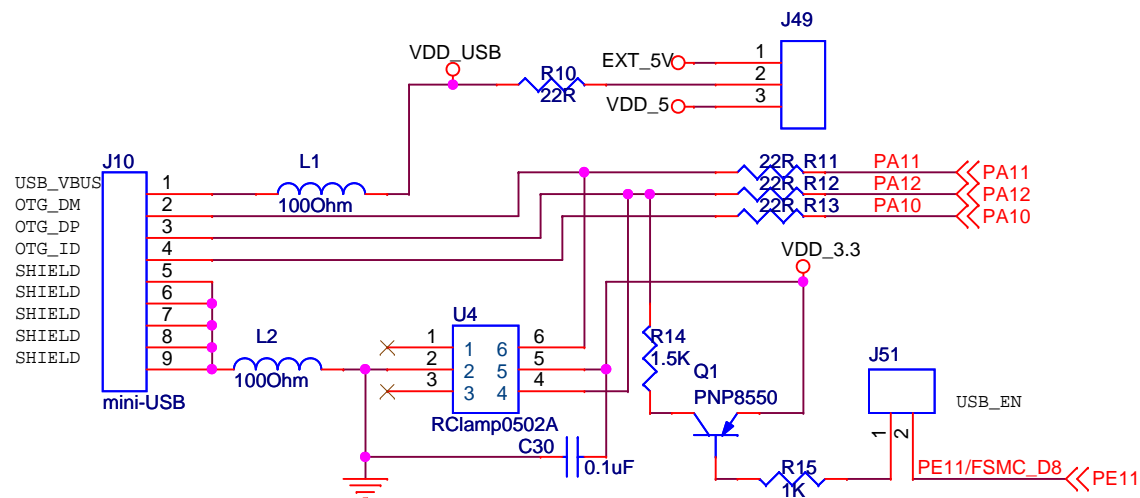
CAN1 Interface



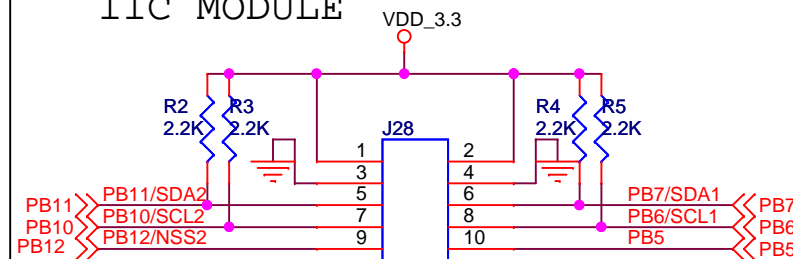
CAN2 Interface(only 107)

USB MODULE

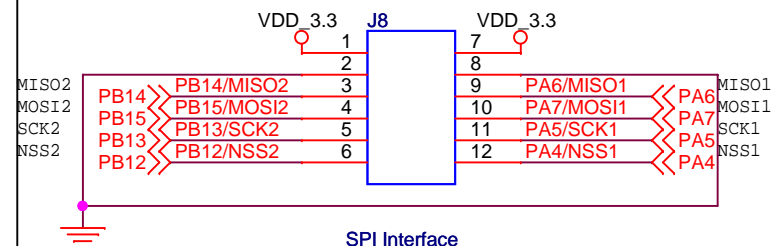
OTG_VBUS



IIC MODULE



SPI MODULE



SPI Interface

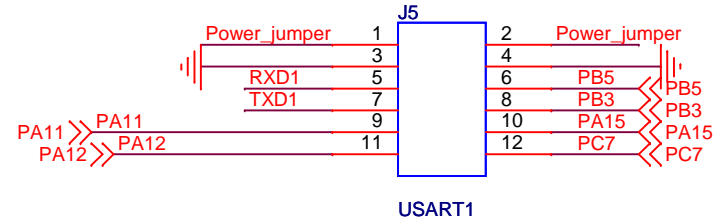
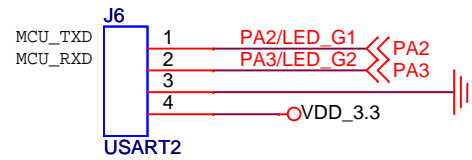
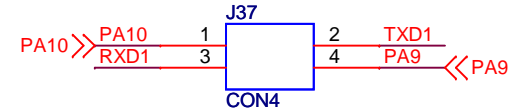
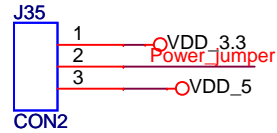
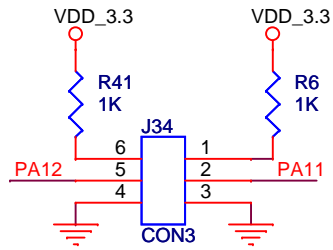
Title Project Name: STM32F103ZE EVB_Base Board

Size A Document Number Communicate Interface1

Rev 1.0

Date: Wednesday, December 05, 2012 Sheet 1 of 8

UART/I2S/IC CARD/FTDI MODULE



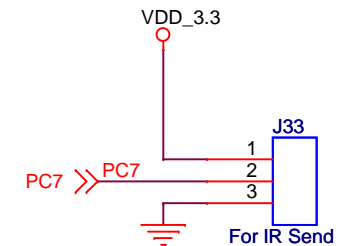
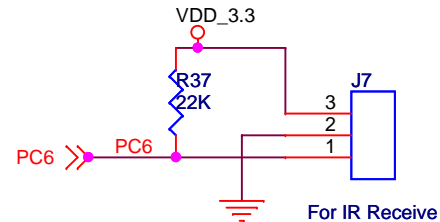
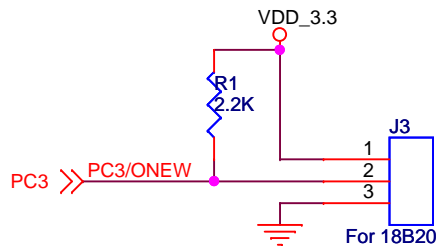
J37 NOTES

MCU_RX -- TARGET_RX: 1-3
MCU_RX -- TARGET_TX: 2-4
MCU_TX -- TARGET_TX: 1-2
MCU_TX -- TARGET_RX: 3-4

J5 PIN OUT

1. Power_Jumper 2. Power
3. GND 4. GND
5. USART1_RXD1 6. I2S3_SD
7. USART1_TXD1 8. I2S3_CK
9. USART1_CTS 10. I2S3_WS
11. USART1_RTS 12. I2S3_MCK

ONE WIRE INTERFACE



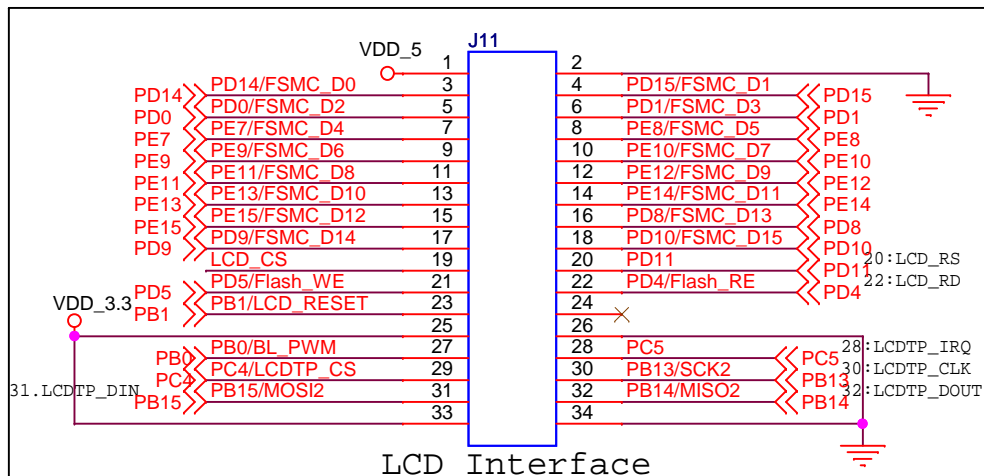
Title Project Name: STM32F103ZE EVB_Base Board

Size A Document Number Communicate Interface2

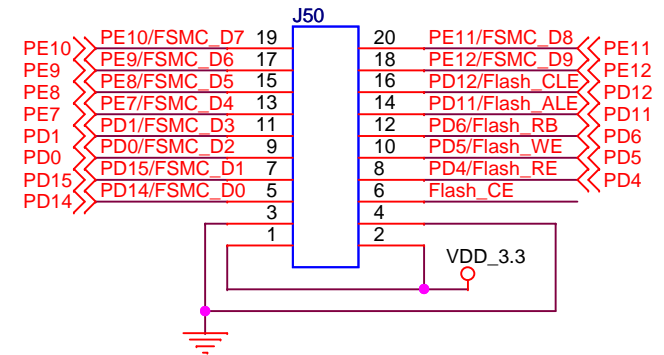
Rev 1.0

Date: Sunday, April 28, 2013

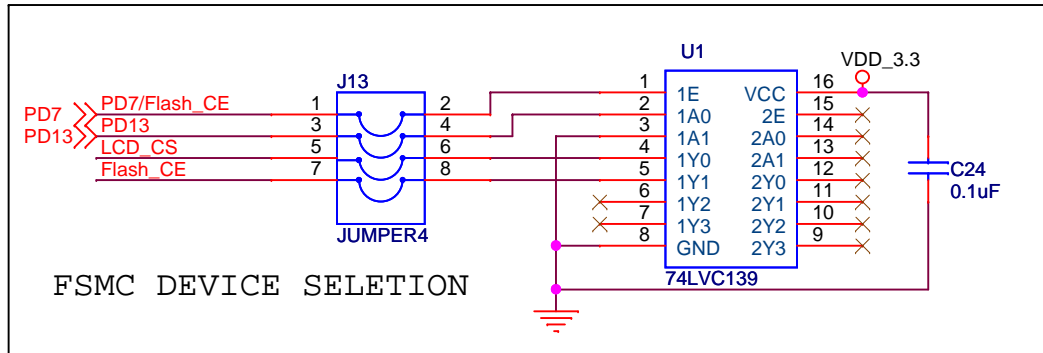
Sheet 2 of 8



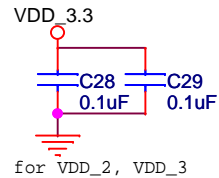
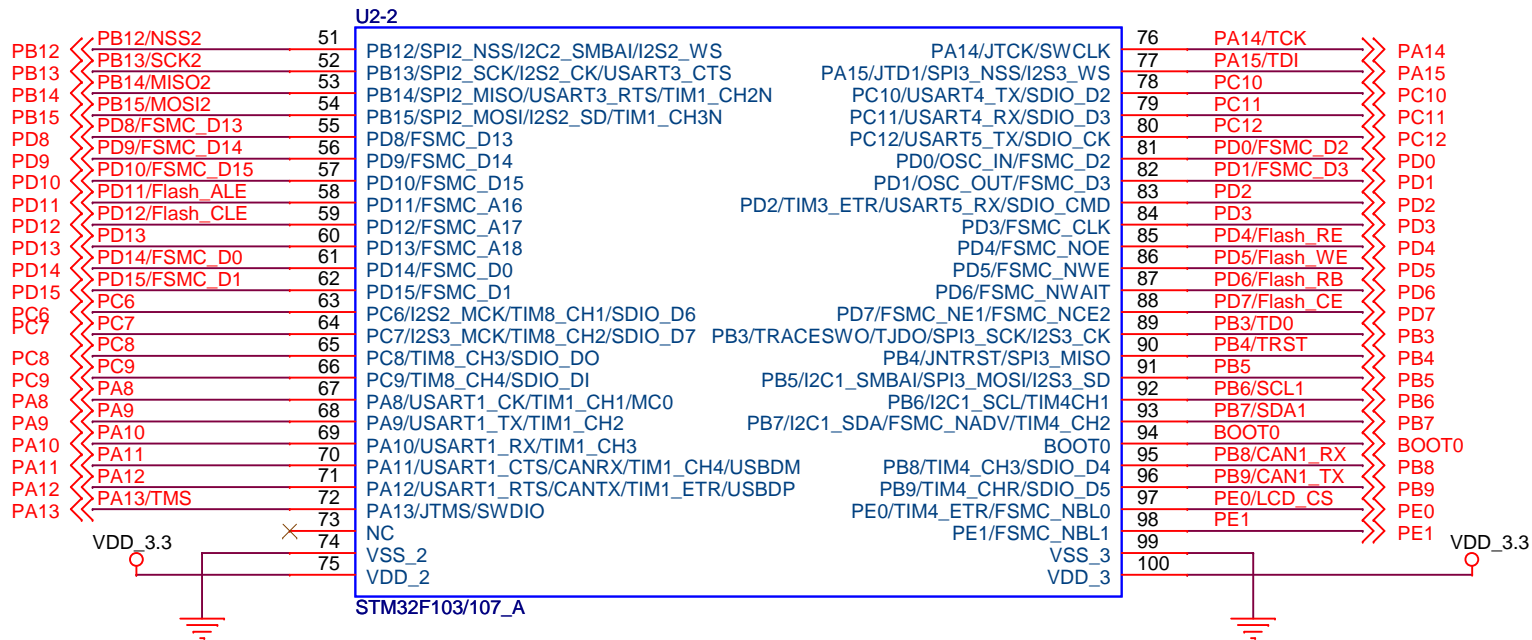
1. NC	2. GND	19. LCD_CS	20. LCD_RS
3. DATA0	4. DATA1	21. LCD_R/W	22. LCD_RD
5. DATA2	6. DATA3	23. LCD_RESET	24. NC
7. DATA4	8. DATA5	25. BL_VCC	26. BL_GND
9. DATA6	10. DATA7	27. BL_PWM	28. LCDTP_IRQ
11. DATA8	12. DATA9	29. LCDTP_CS	30. LCDTP_CLK
13. DATA10	14. DATA11	31. LCDTP_DIN	32. LCDTP_DOUT
15. DATA12	16. DATA13	33. VDD_3.3	34. GND
17. DATA14	18. DATA15		



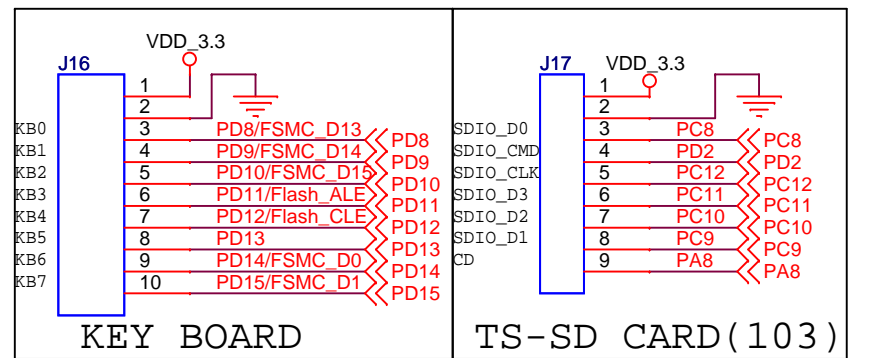
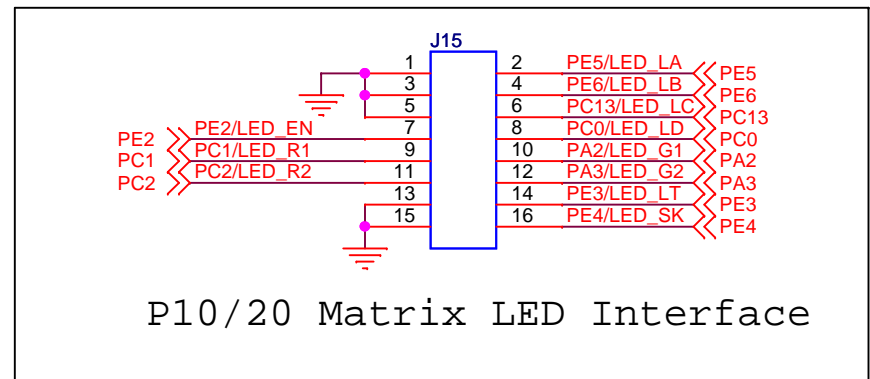
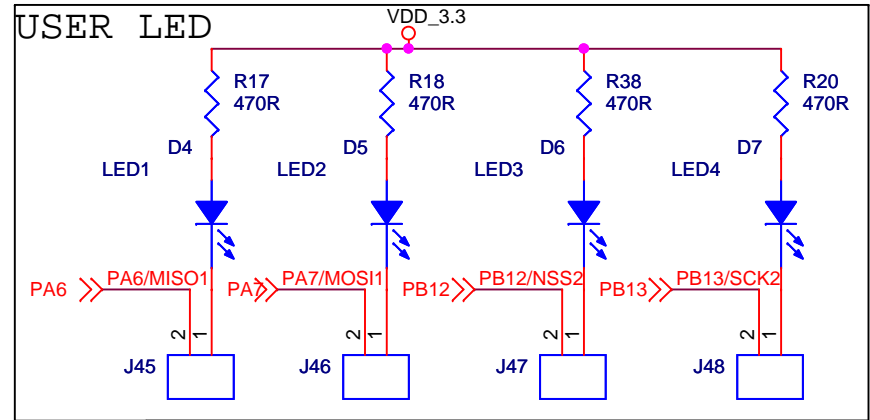
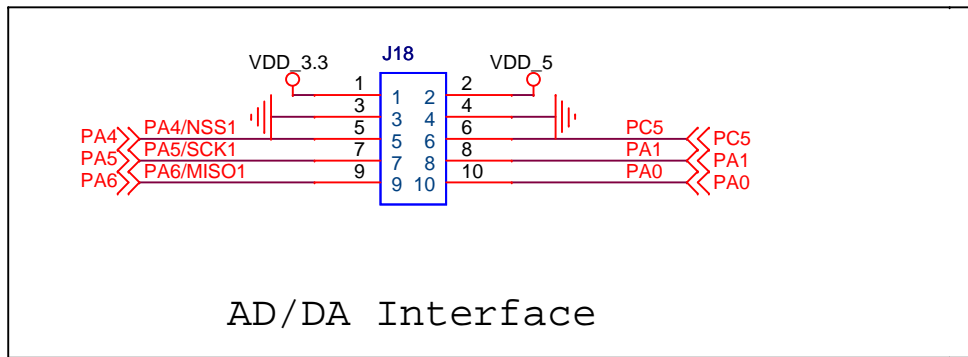
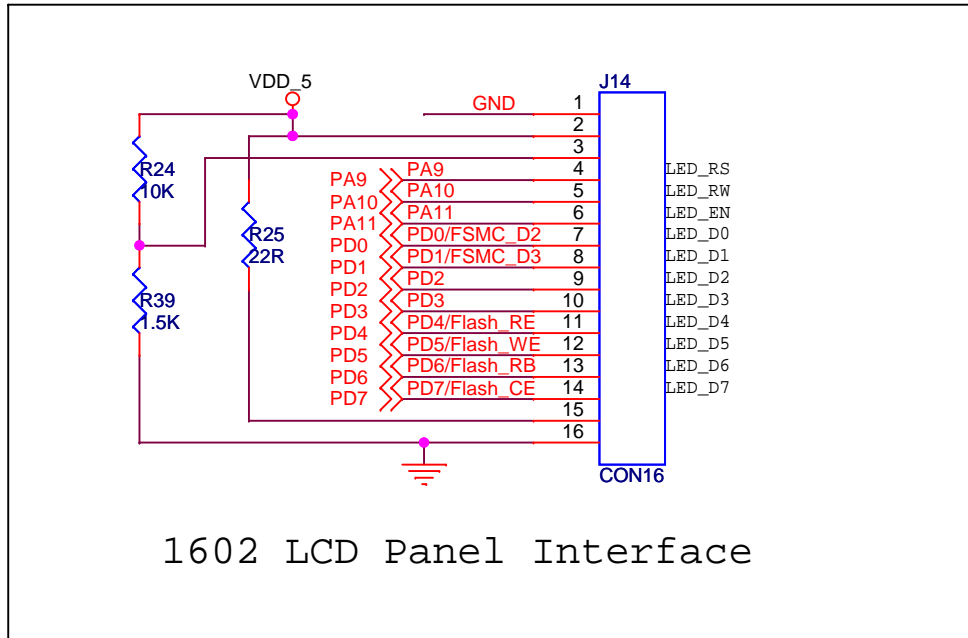
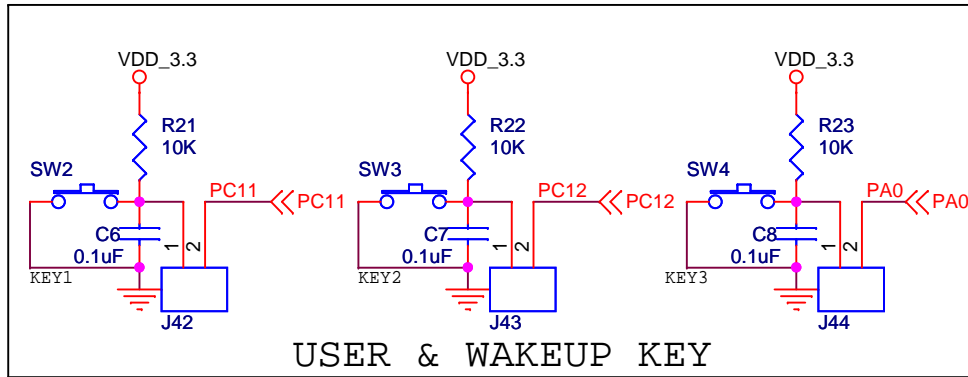
1. VDD_3.3	2. VDD_3.3
3. GND	4. GND
5. DATA0	6. FLASH_CE
7. DATA1	8. FLASH_RE
9. DATA2	10. FLASH_WE
11. DATA3	12. FLASH_RB
13. DATA4	14. FLASH_ALE
15. DATA5	16. FLASH_CLE
17. DATA6	18. DATA9
19. DATA7	20. DATA8



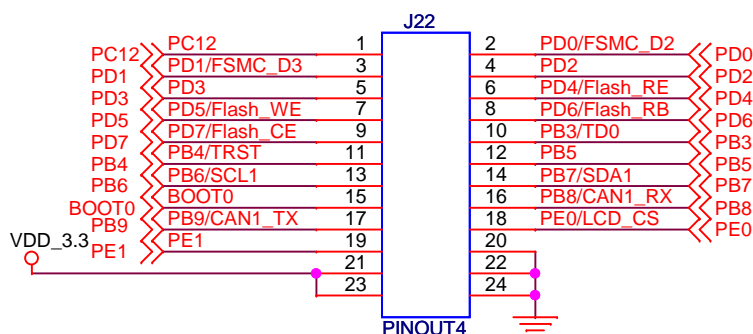
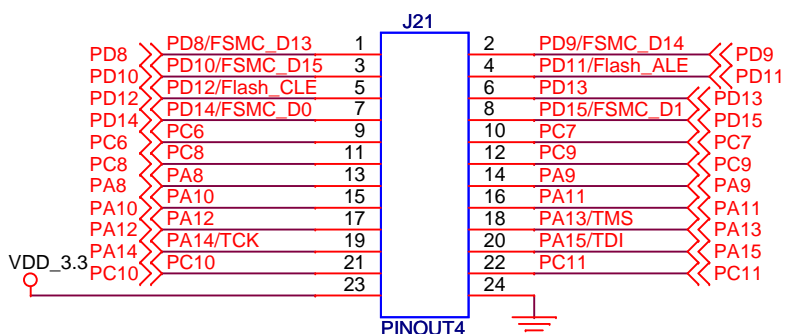
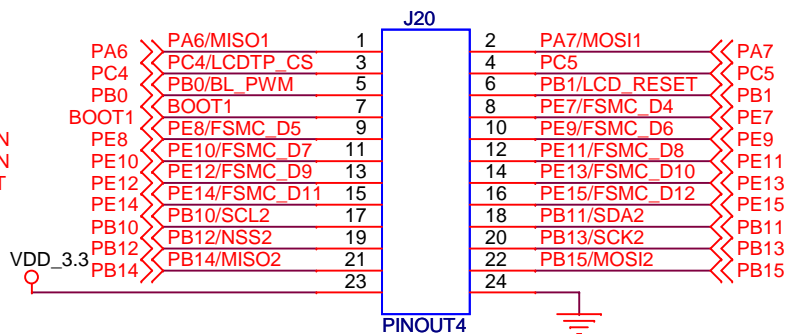
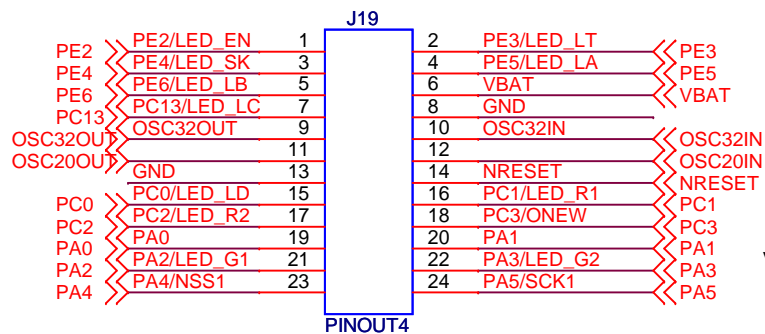
Title		
Project Name: STM32F103ZE EVB_Base Board		
Size A	Document Number FSMC_Device	Rev 1.0
Date:	Tuesday, December 04, 2012	Sheet 3 of 8



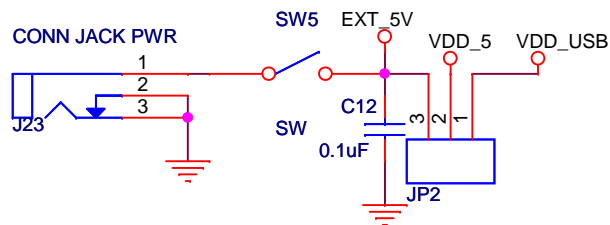
Title		
Project Name: STM32F103ZE EVB_Base Board		
Size A	Document Number MCU_Main2	Rev 1.0
Date:	Tuesday, December 04, 2012	Sheet 5 of 8



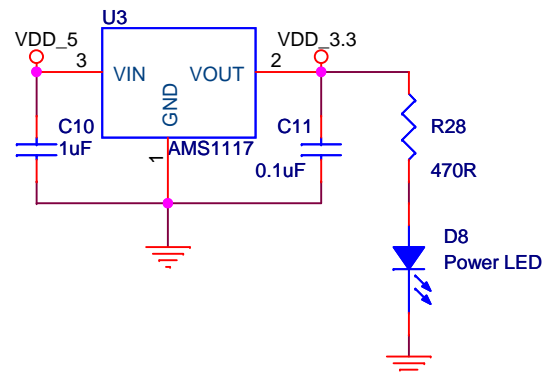
Title		
Project Name: STM32F103ZE EVB_Base Board		
Size A	Document Number Other Interface	Rev 1.0
Date:	Friday, December 07, 2012	Sheet 6 of 8



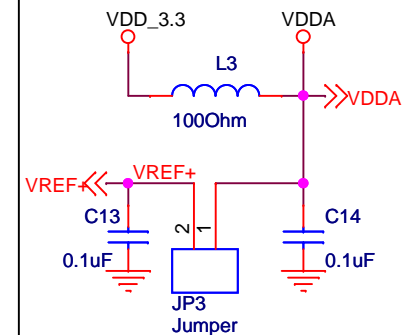
VDD_5 SELECTION



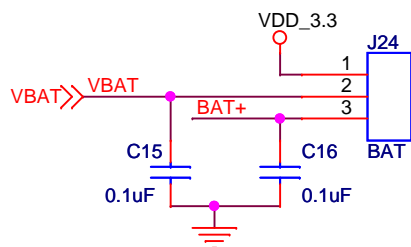
VDD_5 TO VDD_3.3



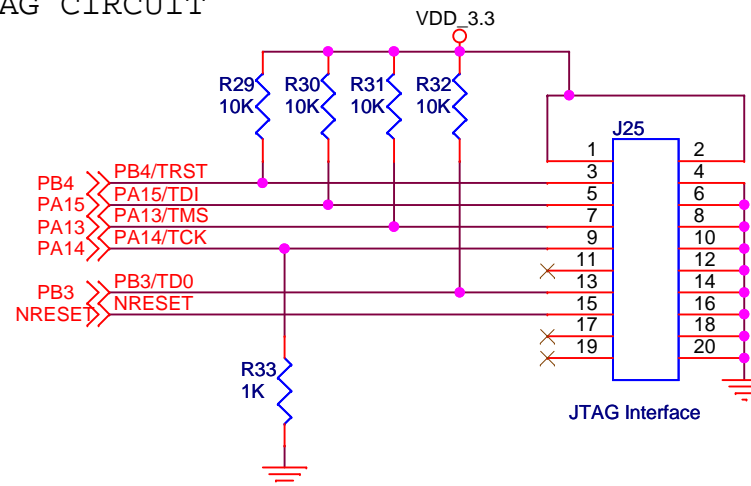
VDDA SELECTION



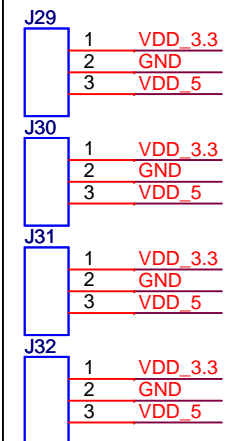
VBAT SELECTION



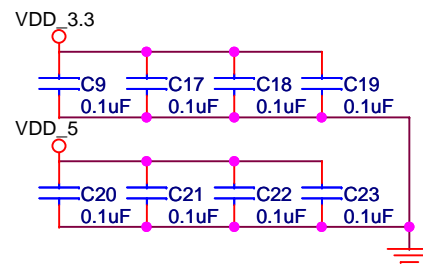
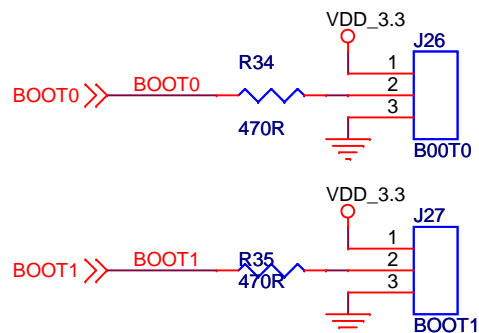
JTAG CIRCUIT



POWER SETS



BOOT SELECTION



Title		
Project Name: STM32F103ZE EVB_Base Board		
Size A	Document Number POWER&BOOT&JTAG	Rev 1.0
Date:	Friday, December 07, 2012	Sheet 8 of 8